

Eval-FE50MLIR

DC-50 MBd LC Evaluation Kit

User Guide



EVALUATION KIT CONTENTS

The Evaluation Kit contains the following:

1. Evaluation PCB
2. FE50MLIR mounted onto the evaluation PCB
3. POF cable with loop back LC plug (1 m, 0.5 NA, 2.2 mm jacket simplex POF)
4. FE50MLIR Datasheet

INITIAL SETUP

1. Connect GND of a DC power supply to the ground points of the PCB (black terminals).
2. Connect 3.3 / 5 V to each of the Tx and Rx VCC jacks (red terminals).
3. To measure common GND, connect a probe to the test points TP1 (Tx) and TP2 (Rx).
4. Connect suitable pattern generator signal via an SMA cable to the TXD data pin.
5. Connect the RXD data pin (TTL output) to a suitable high-speed oscilloscope using 1 M Ω termination and high-speed coax, SMA terminated cable.
6. For a loop-back cable test, connect the provided LC loop-back cable assembly into the LC connector. This connects the Tx back to the Rx over 1m of Step-Index POF.

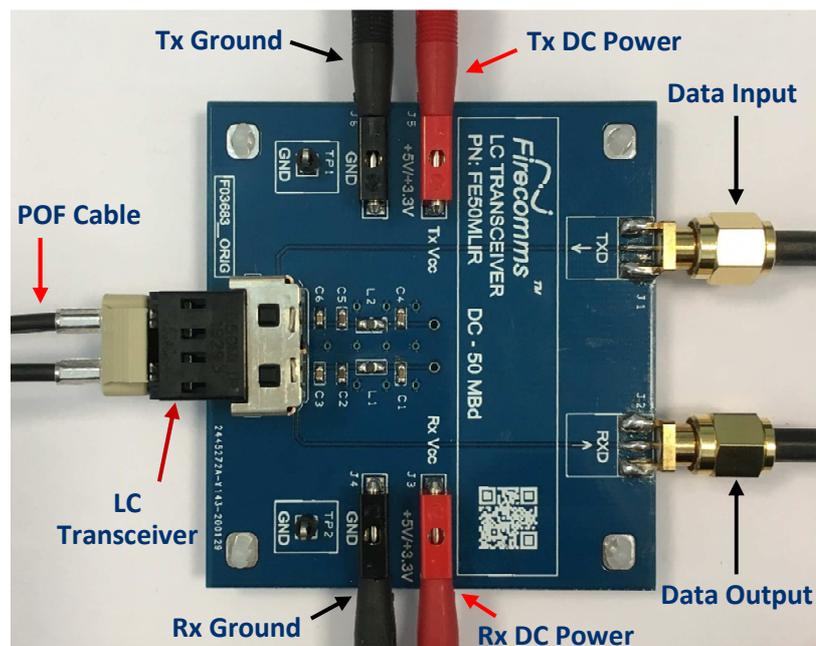


FIGURE 2
Setup of the FE50MLIR Evaluation PCB